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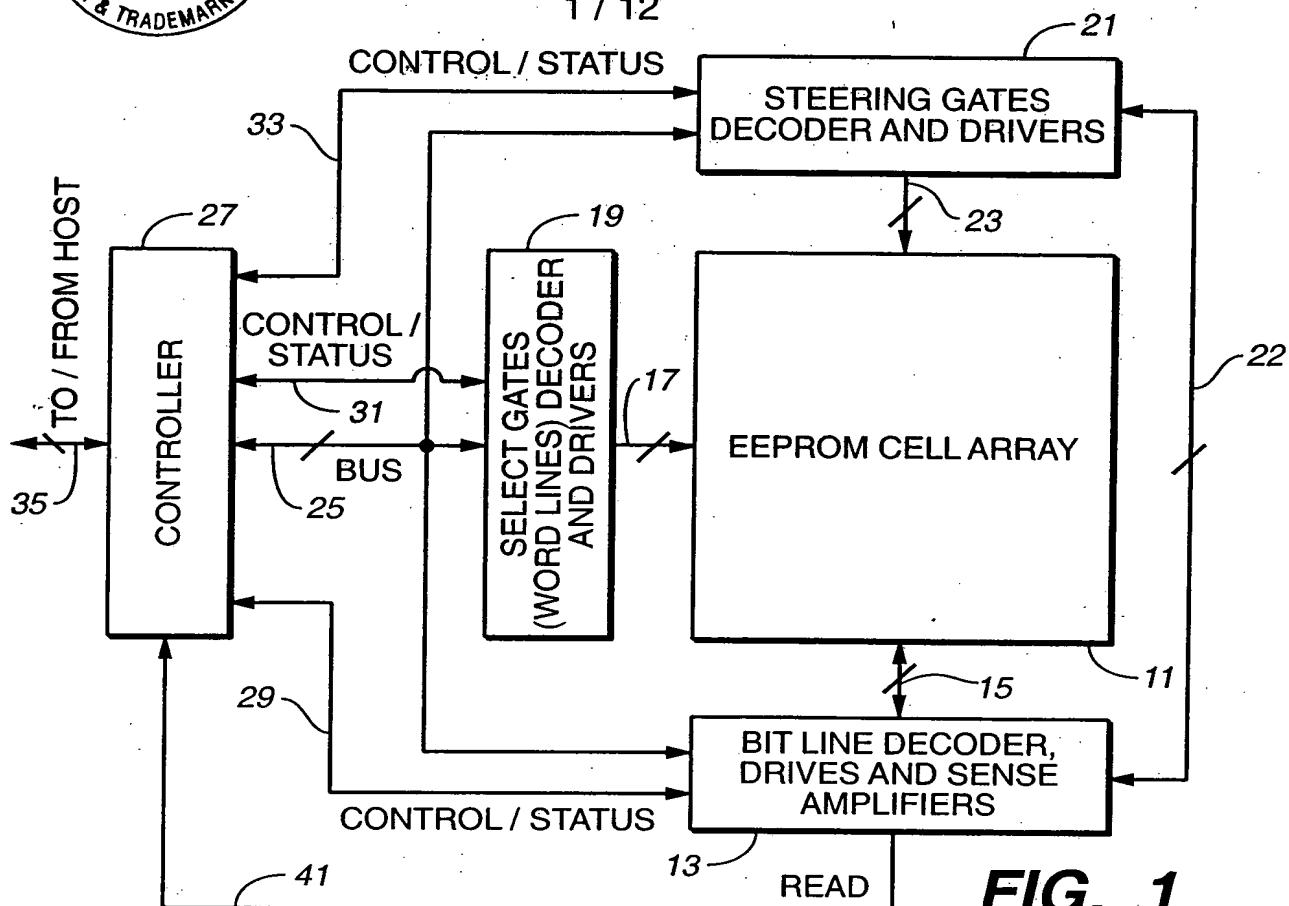


FIG.\_1

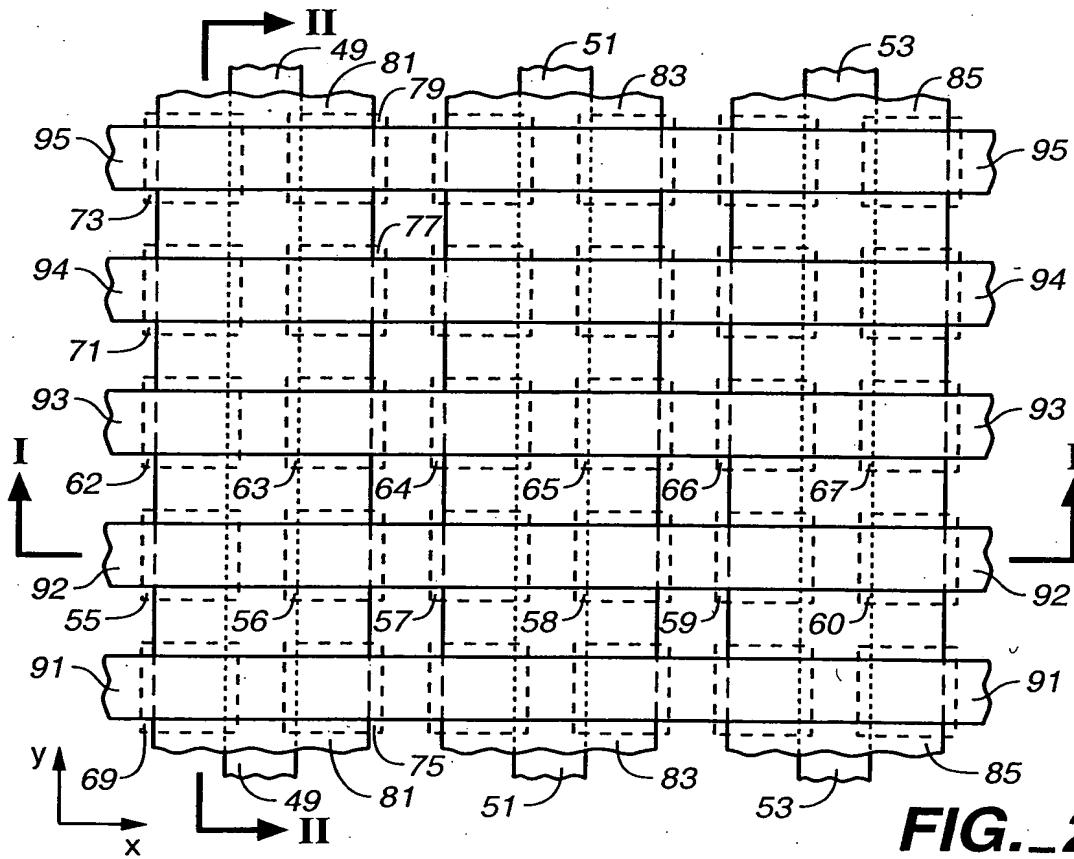


FIG.\_2



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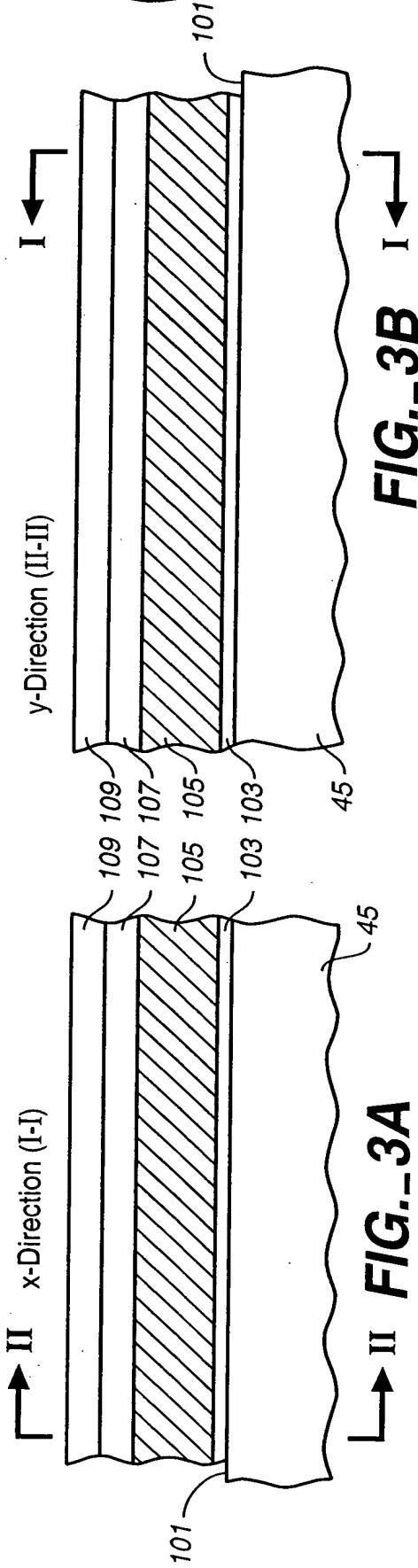
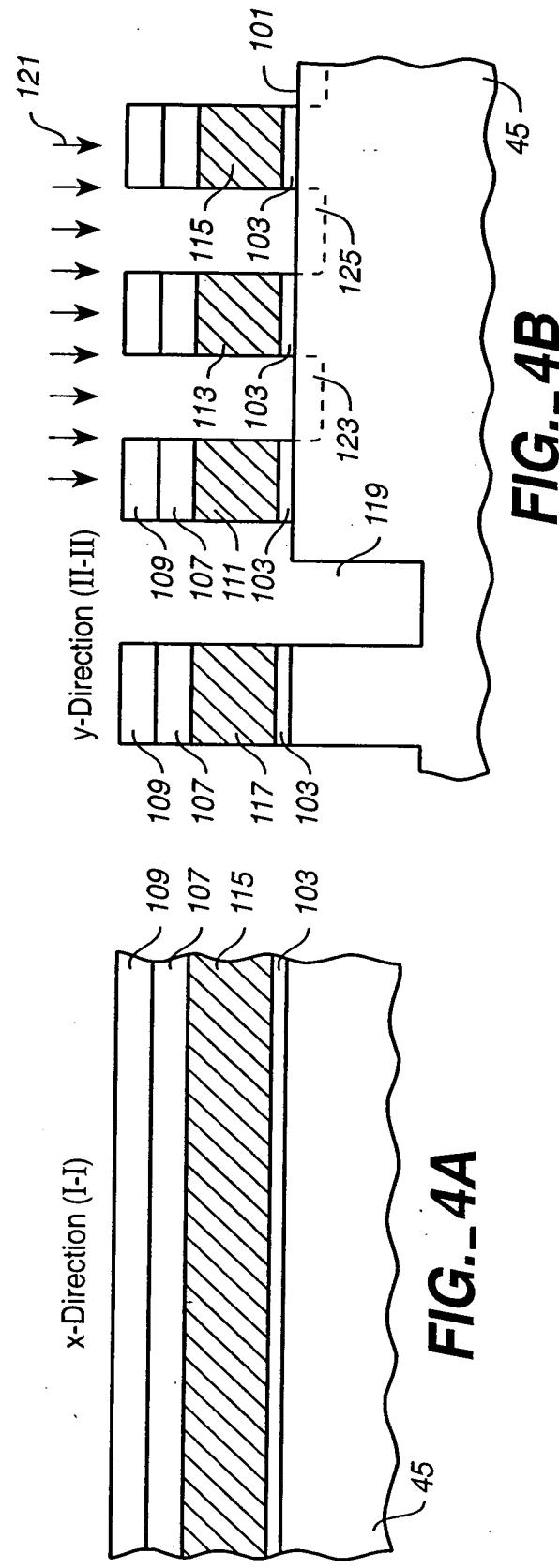
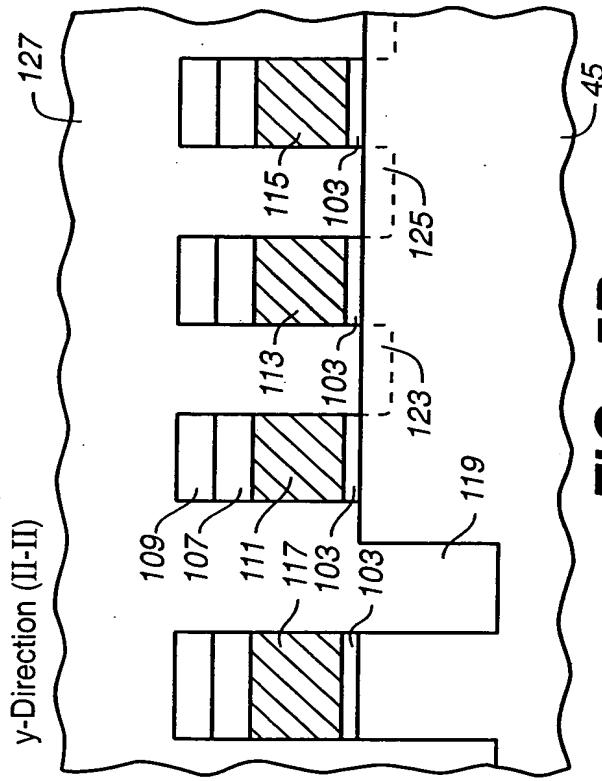
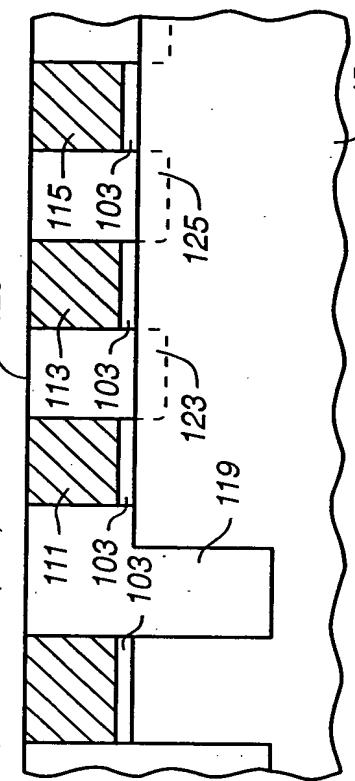


FIG. 3B

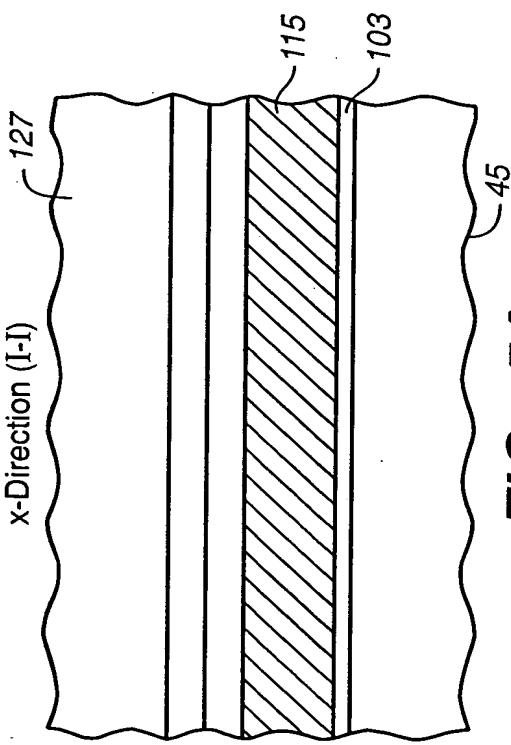




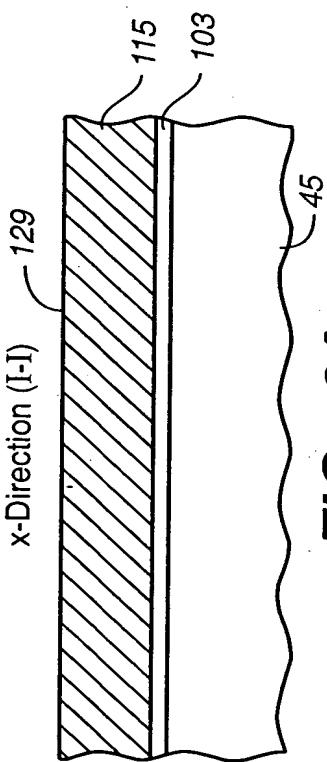
**FIG.-5B**



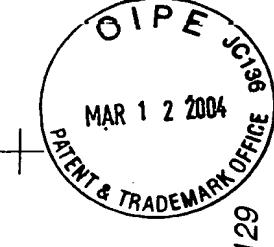
**FIG.-6B**



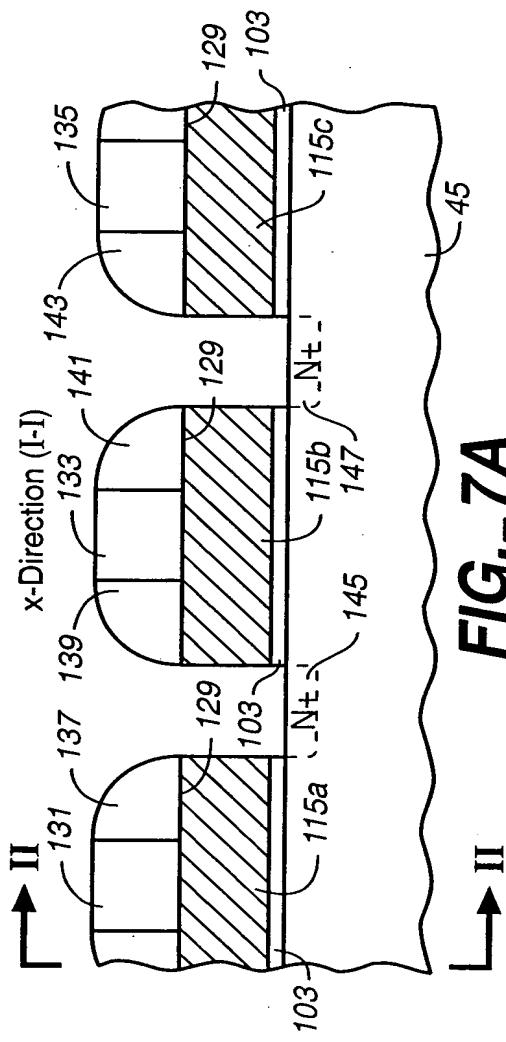
**FIG.-5A**



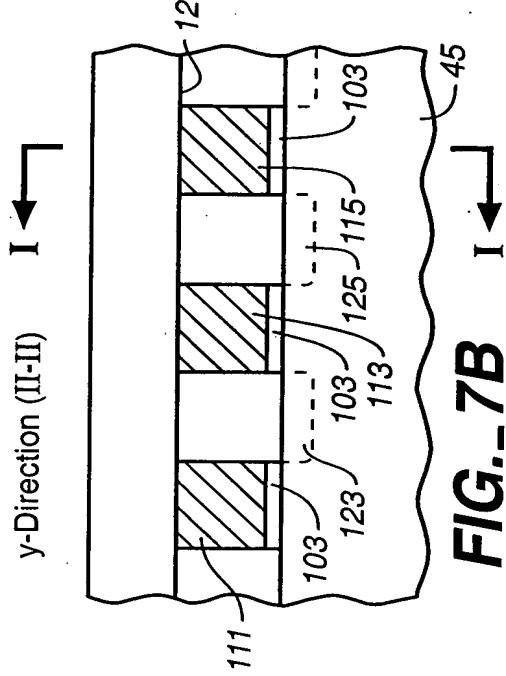
**FIG.-6A**



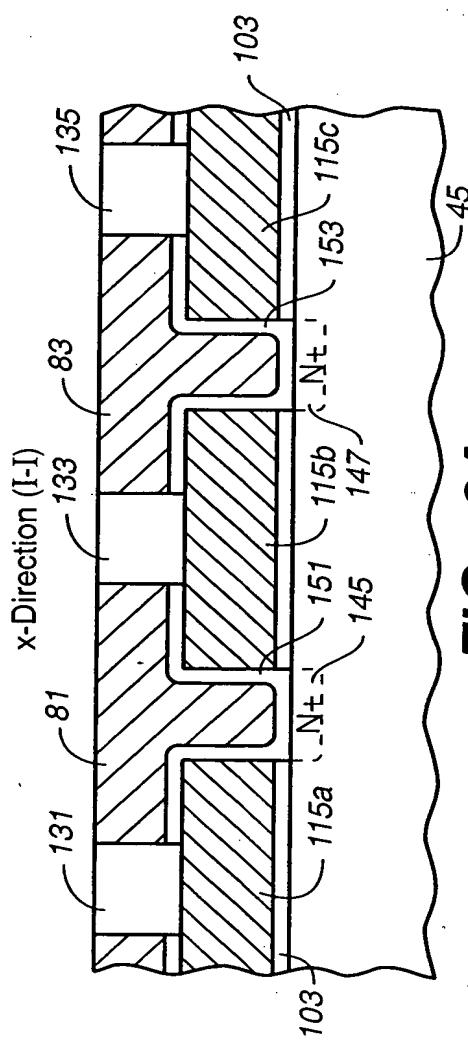
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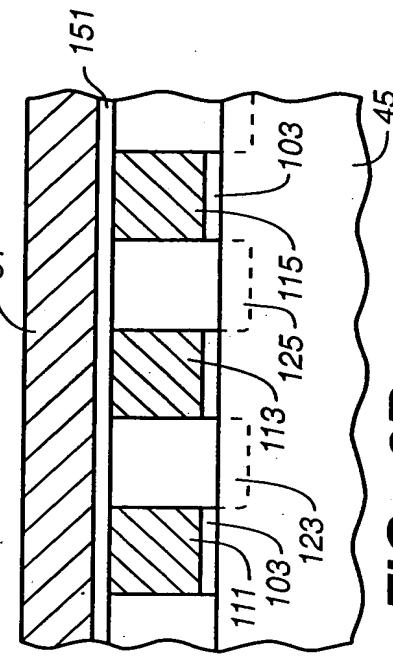
**FIG.\_7A**



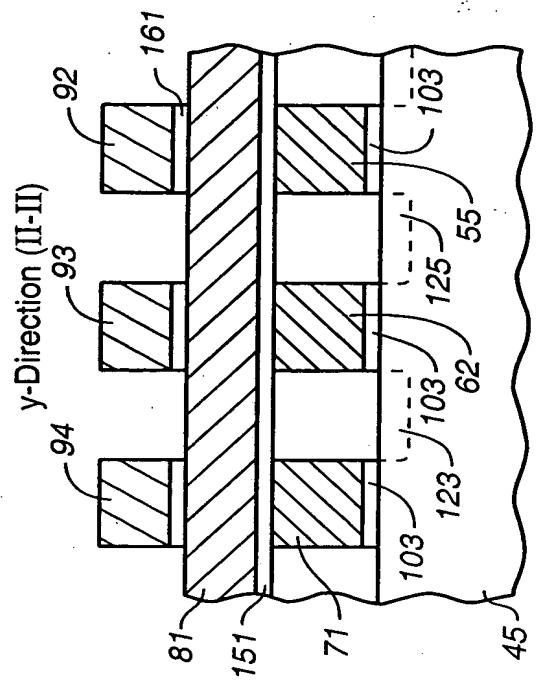
**FIG.\_7B**



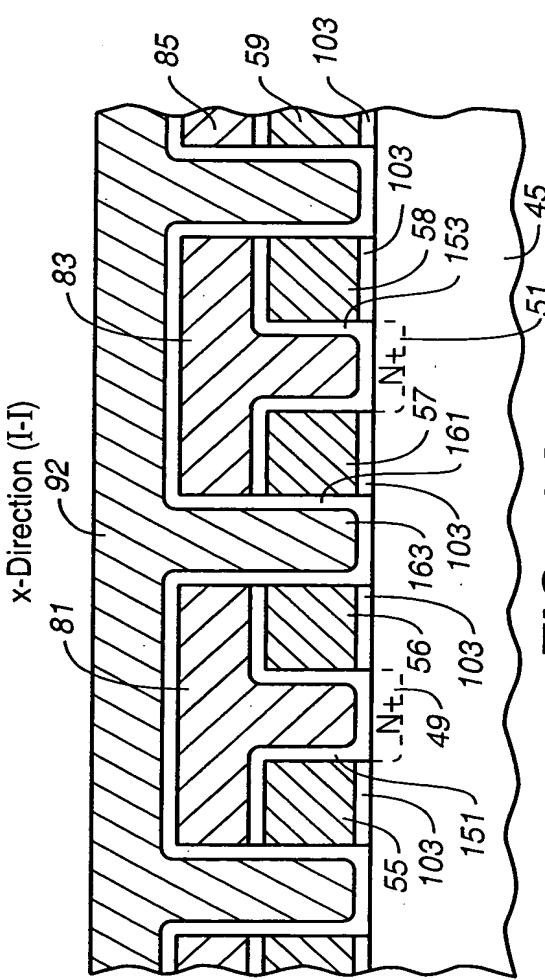
**FIG.\_8A**



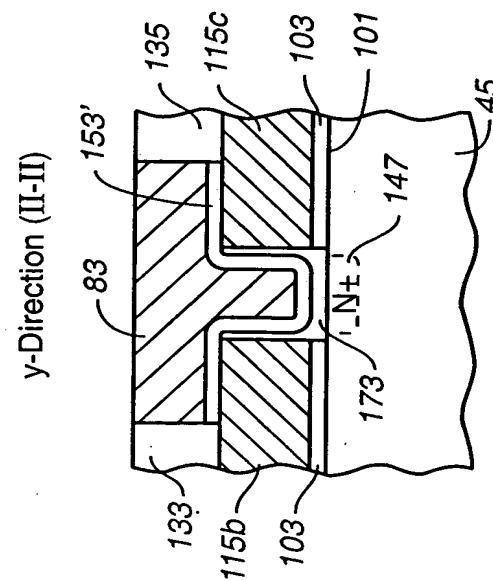
**FIG.\_8B**



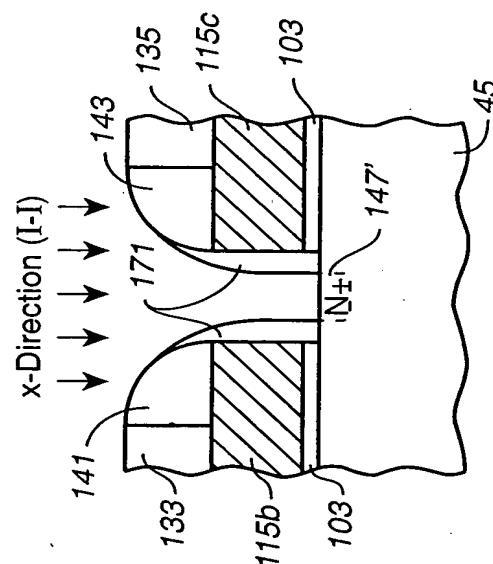
**FIG.\_9B**



**FIG.\_9A**



**FIG.\_11**

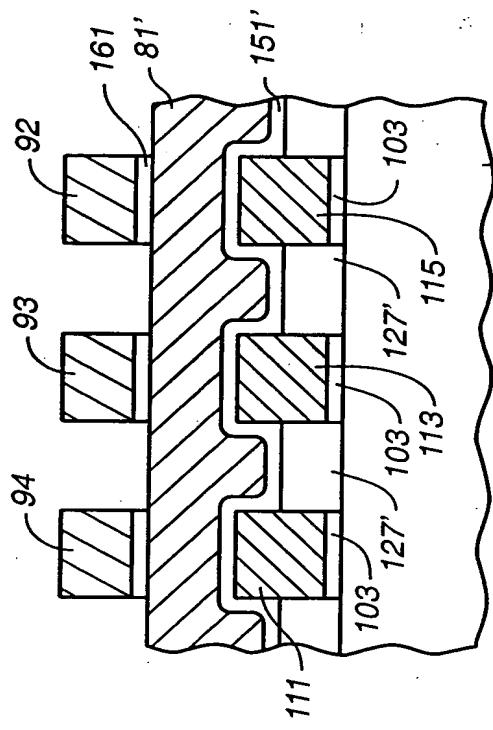


**FIG.\_10**

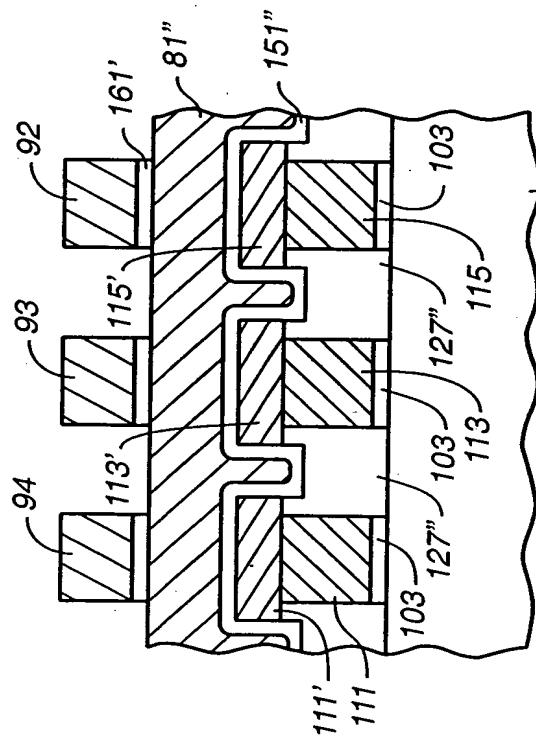
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Appln. No.: 09/925,102 Docket No.: SNDK.257US0  
Title: Scalable Self-Aligned Dual Floating Gate Memory...  
Inventors: Yuan et al. Conf. No.: 3186  
Filing Date: 8/8/01 Atty. Tel: (415) 318-1160 Sheet 6 of 12

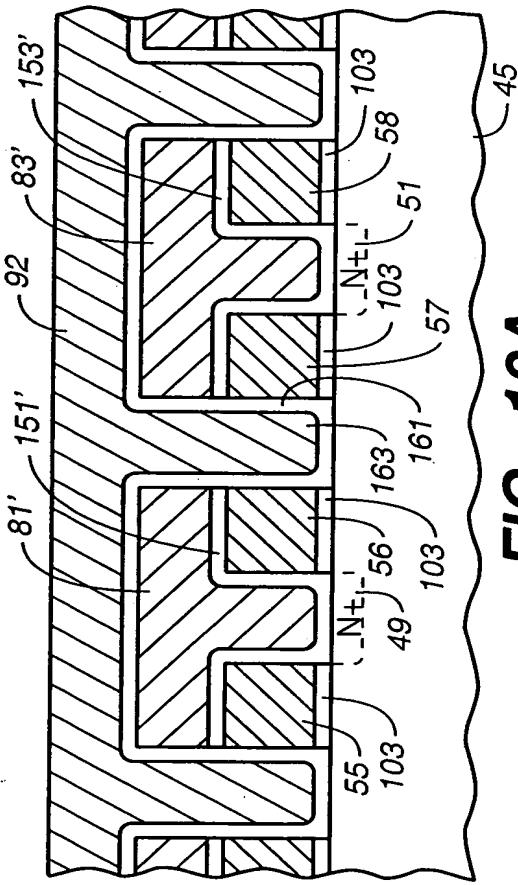
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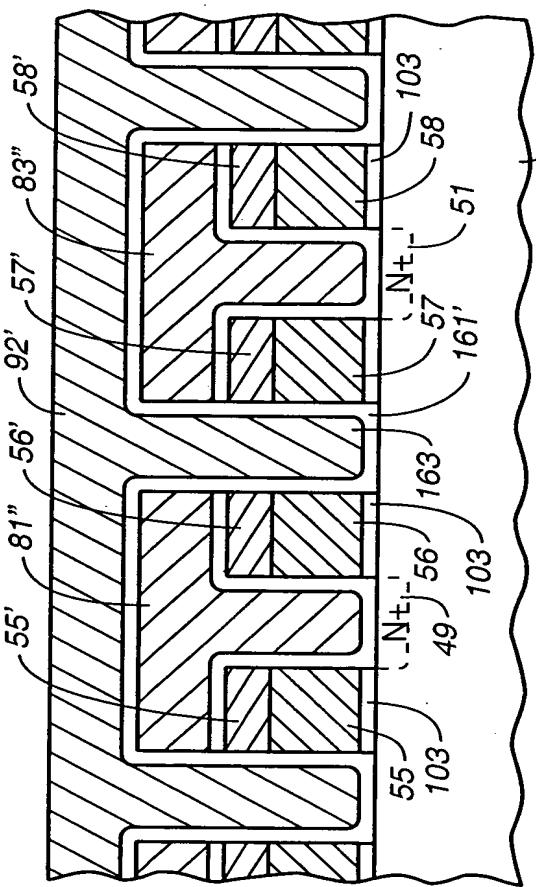
**FIG. 12B**



**FIG. 13B**



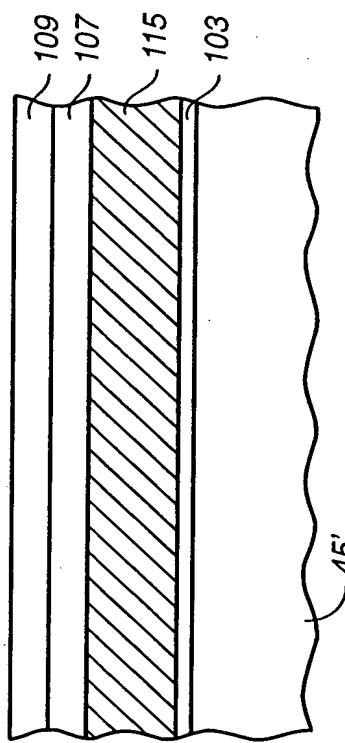
**FIG. 12A**



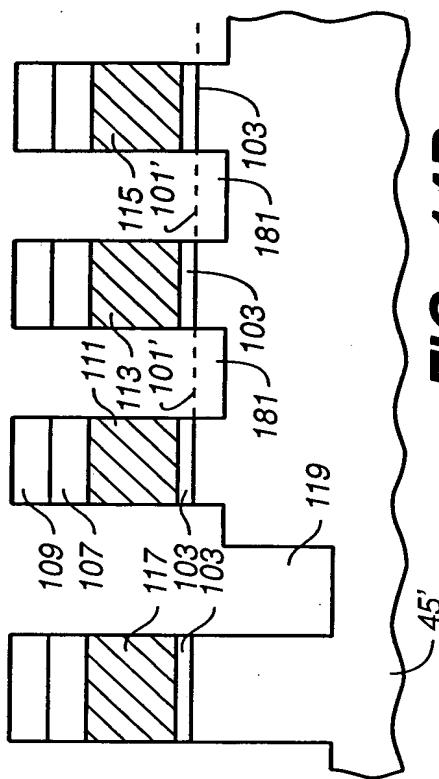
**FIG. 13A**



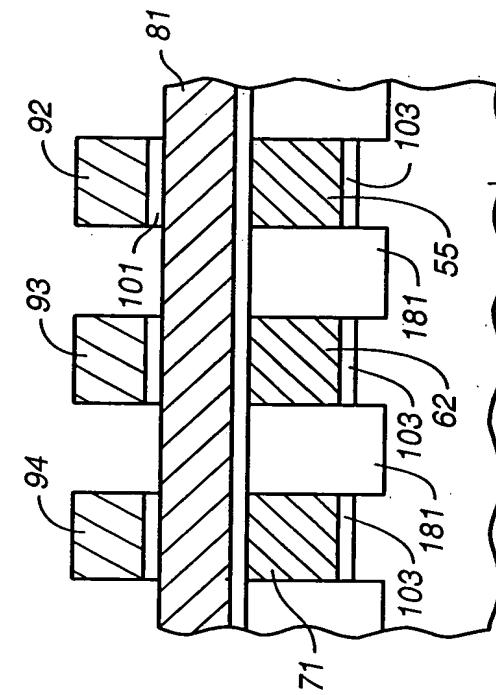
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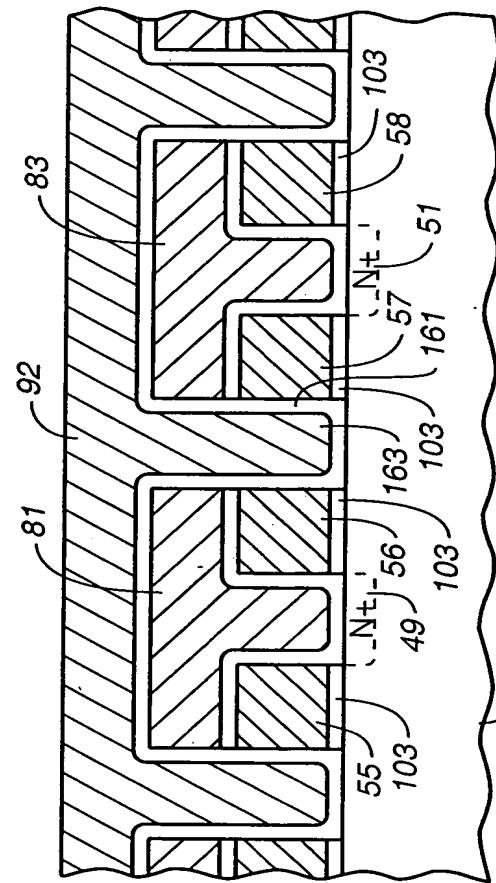
**FIG.\_14A**



**FIG.\_14B**



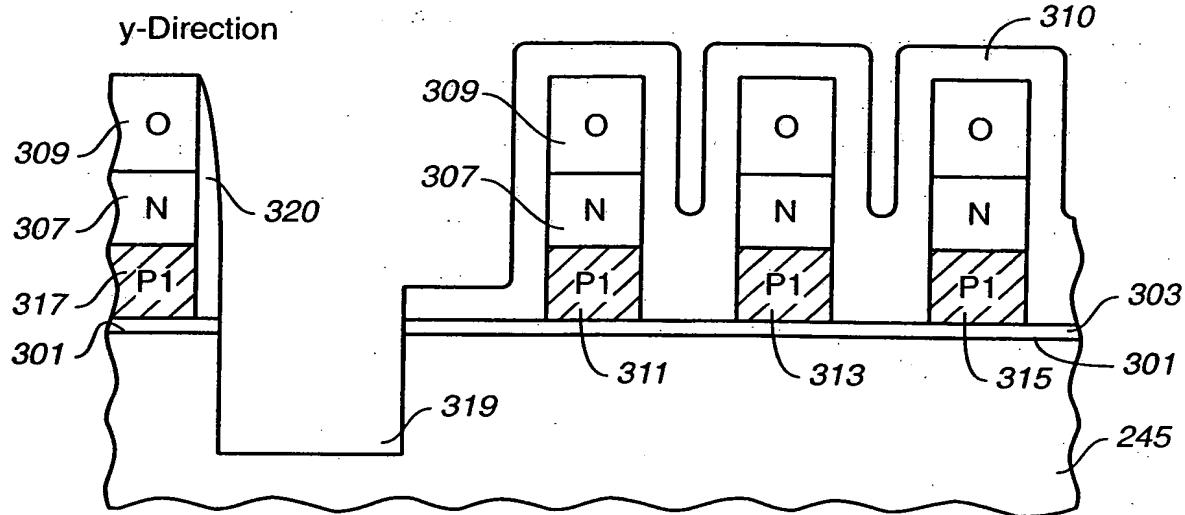
**FIG.\_15B**



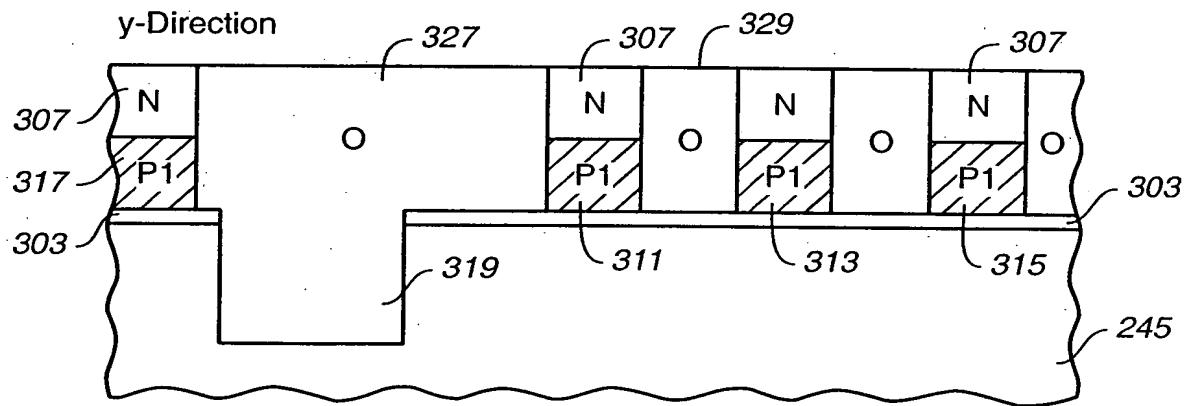
**FIG.\_15A**



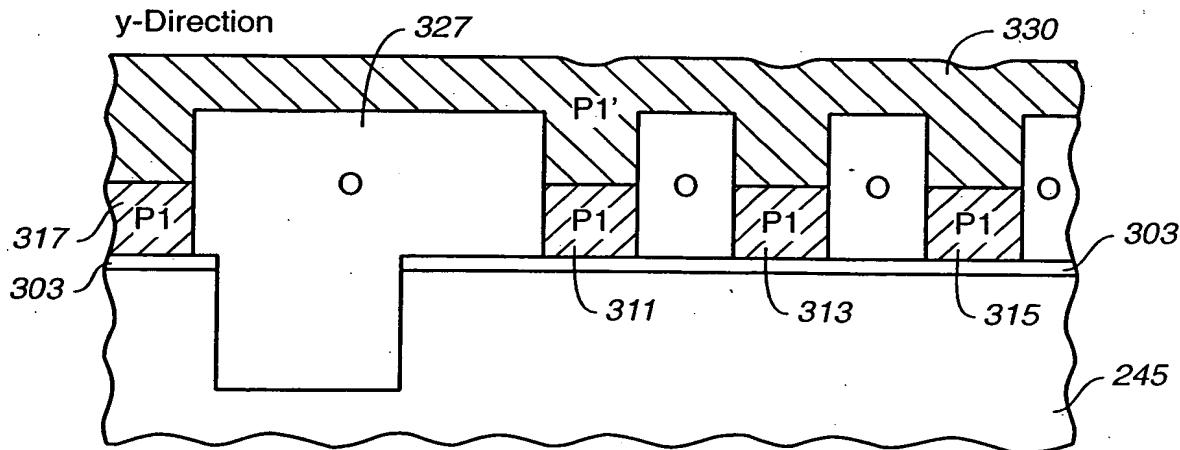
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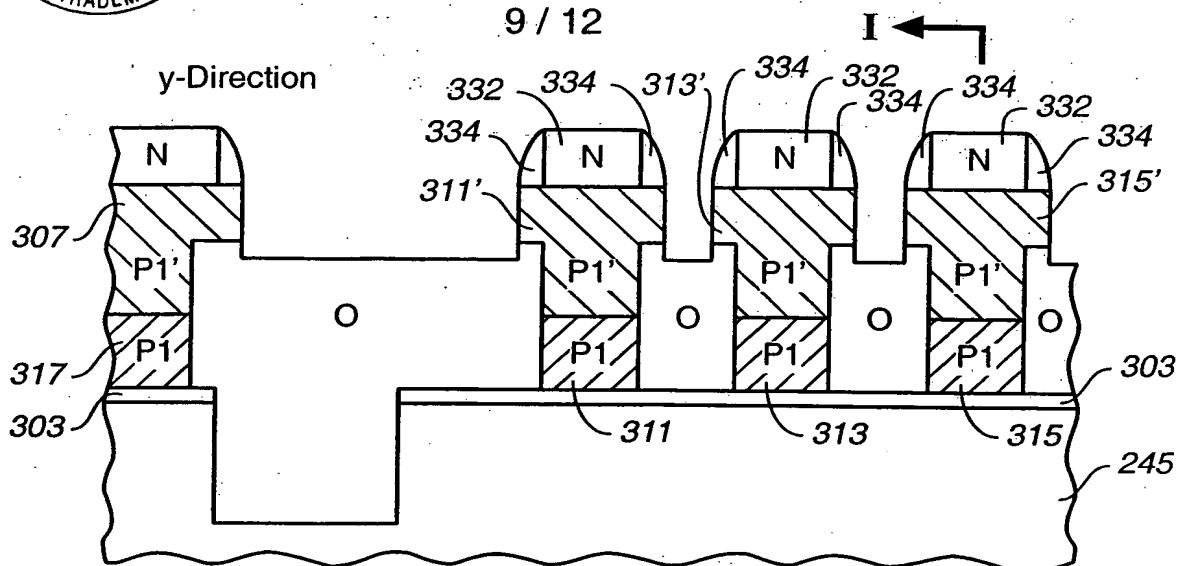
**FIG. 16**



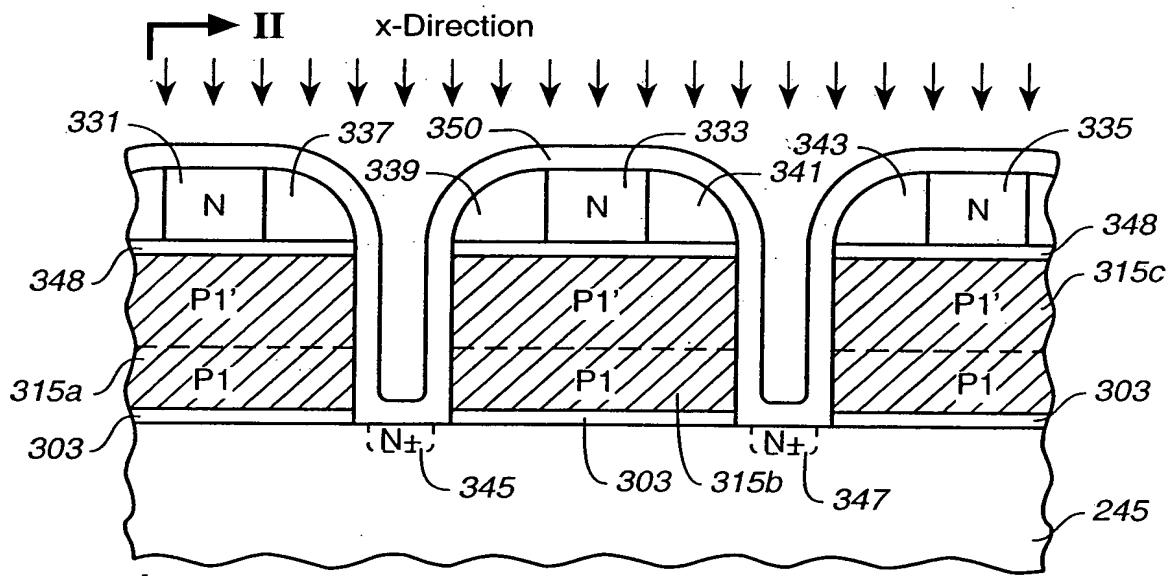
**FIG. 17**



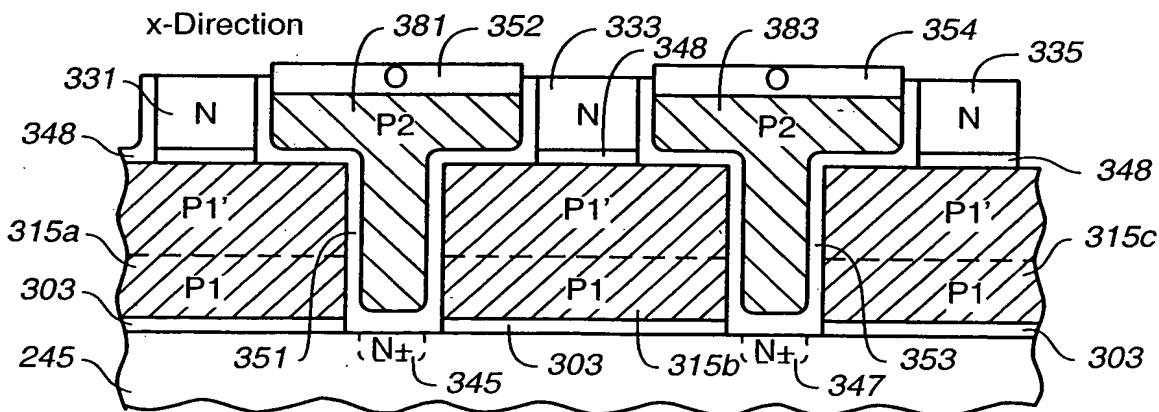
**FIG. 18**



**FIG.\_19**



**FIG.\_20**

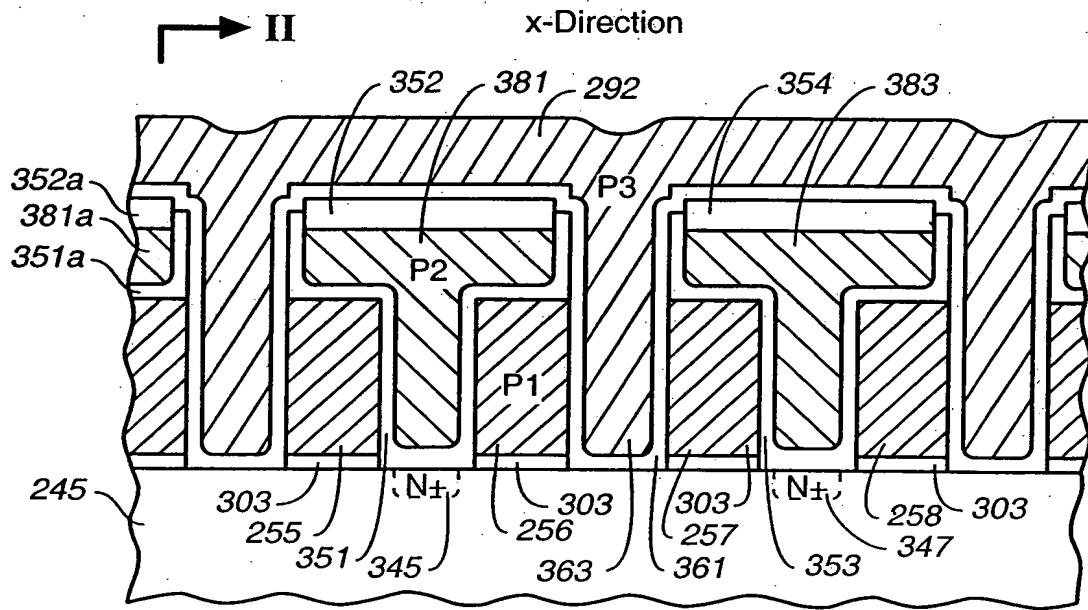


**FIG.\_21**

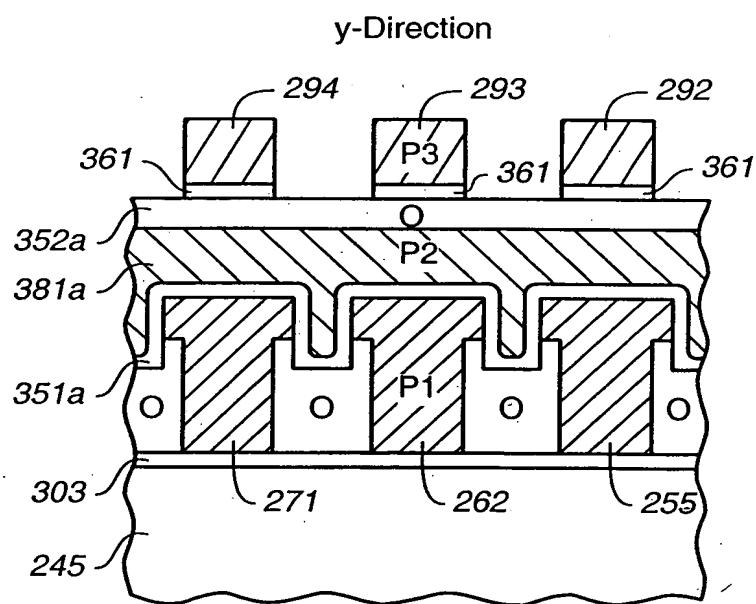


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 Inventors: Yuan et al. Conf. No.: 3186  
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**FIG.\_22A**



**FIG.\_22B**



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Section I-I

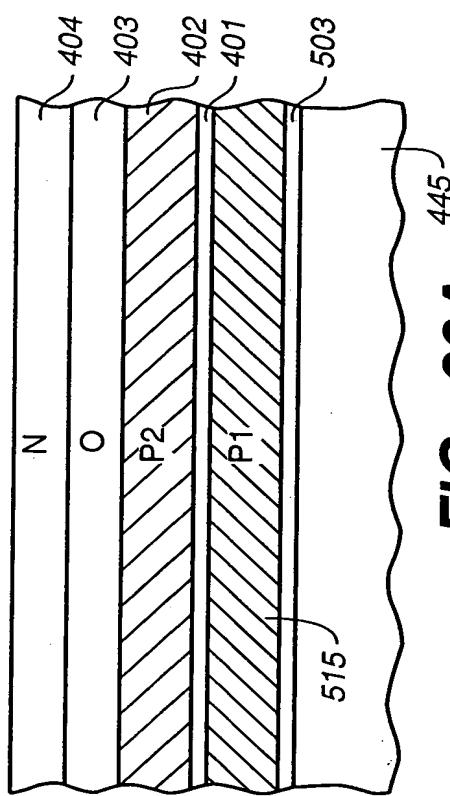


FIG.-23A

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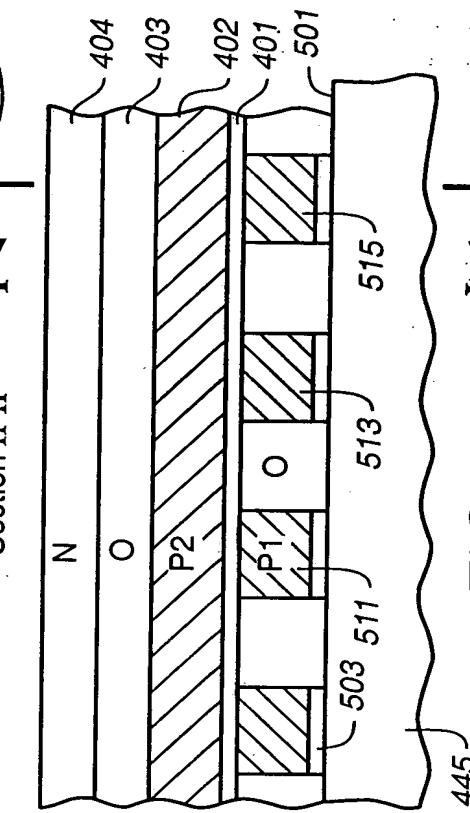


FIG.-23B I

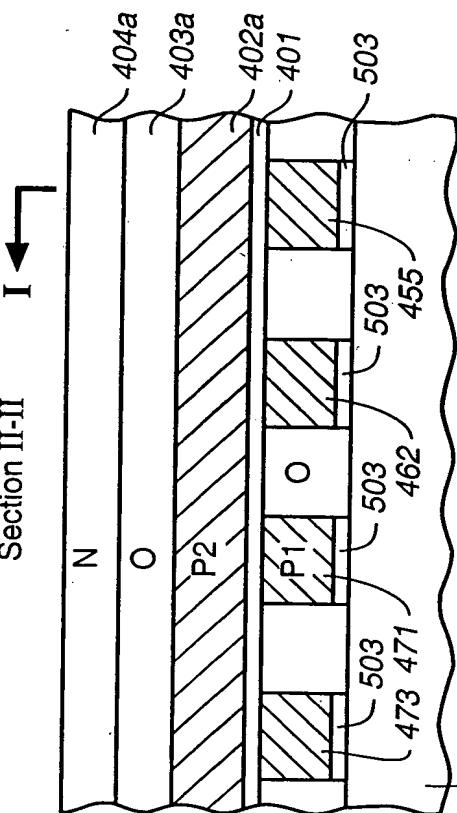


FIG.-24B I

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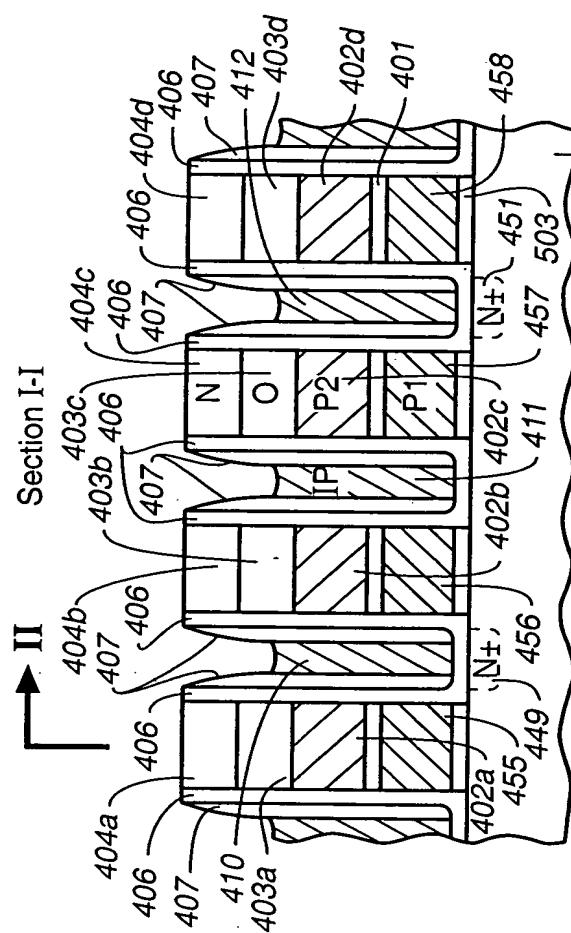
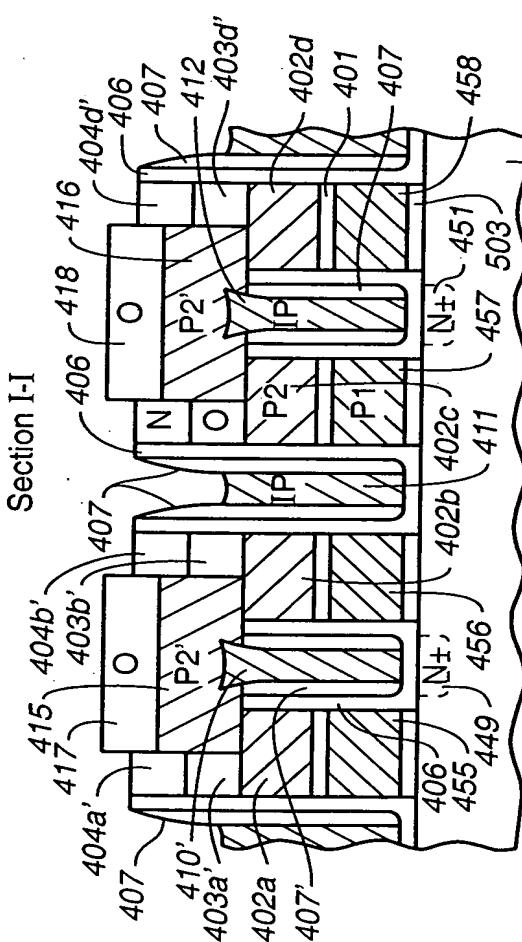


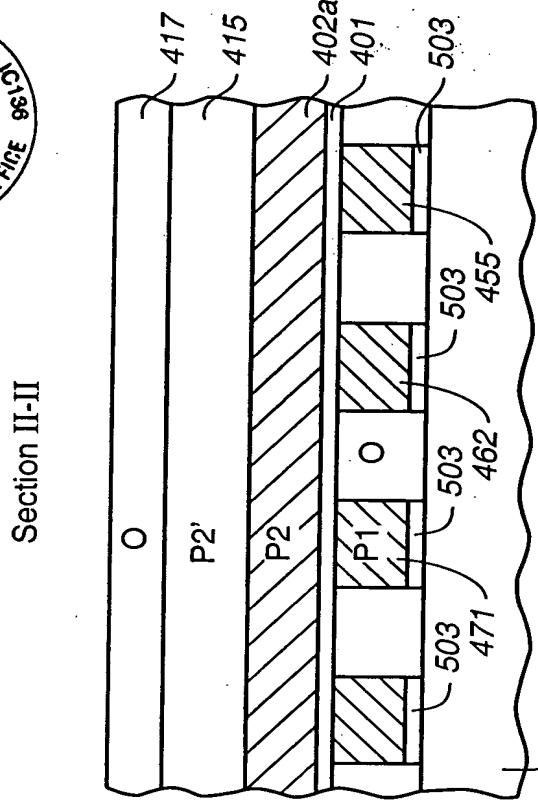
FIG. 24A



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Section III-II

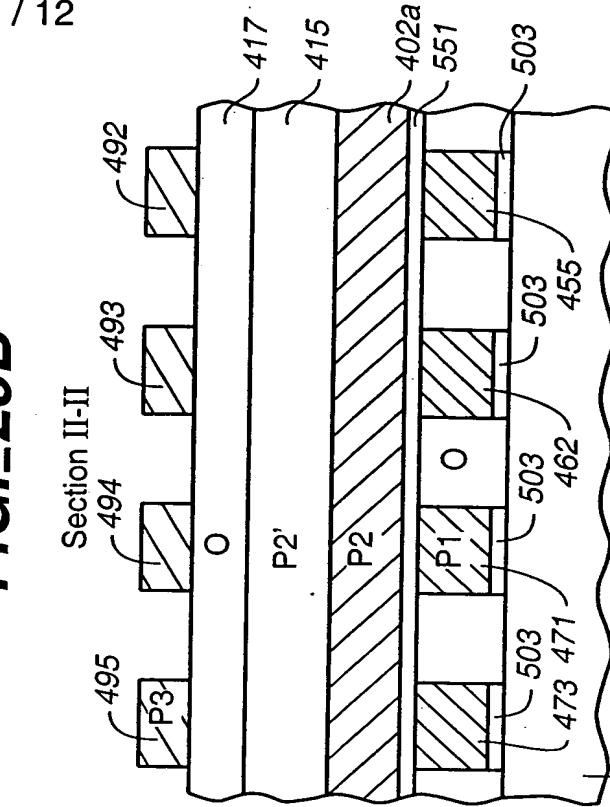
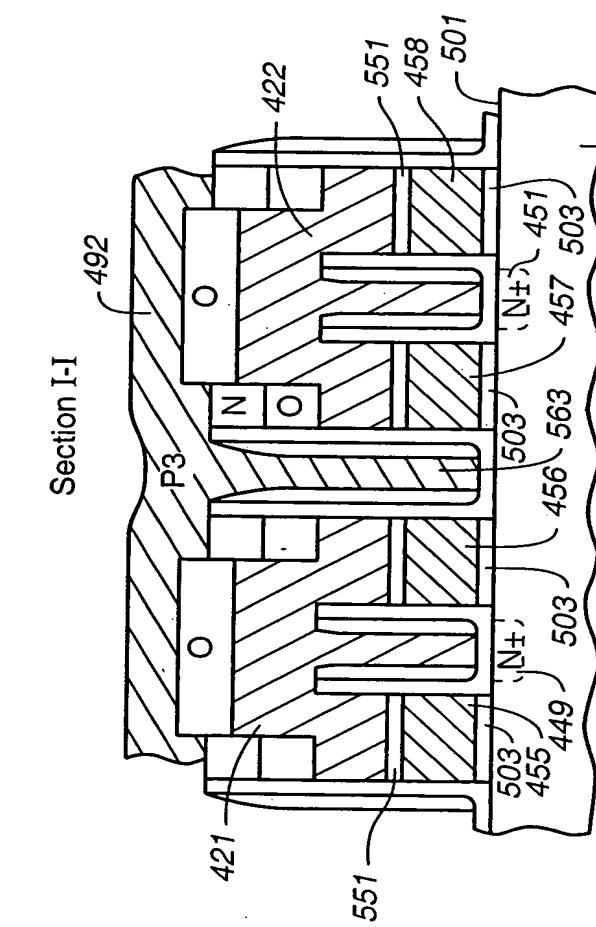


FIG. 25B



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